



Smart Packaging Intern

Center for Packaging and Unit Load Design at Virginia Tech

Description: The Center for Packaging and Unit Load Design (CPULD) is one of the most experienced research centers in the area of package, pallet, and unit load design. The Center, established in 1976, has been actively involved in the development of ISO, ASTM, and MHIA standards related to the transportation of goods, with a focus on environmental responsibility and sound economics.



Our facilities feature a comprehensive pallet and packaging testing lab, and material handling equipment similar to that found in industrial warehouses. The Center's team includes experts in packaging, palletization, material handling and unit load design, who perform contract testing services, continuous education short courses, and contract research.

Research at CPULD focuses on the development of systems-based technologies to optimize the relationship between the design and performance of unit loads, and maximize the efficiency of the complete system. The Center has started to work on the development of an intelligent pallet system and on the implementation of IoT in the distribution packaging sector. We are also starting new undergraduate level classes in the area.

The Smart Packaging Intern position will aid the development of a new pathway class called Intelligent Packaging and the Global World. The intern will be in charge reviewing the latest literature related to the implementation of IoT in the distribution packaging sector. This review will specifically focus on the technical



details of IoT including sensors, networks, applicable processing methods. The intern will also be aid the development of teaching slides and hands-on practices.

Responsibilities:

- Review the latest literature and create teaching aids
- Develop visual aids for power point presentations to aid the explanation of complex concepts
- Develop practical laboratory exercises using systems such as accelerometers and other sensors connected to raspberry pi or similar systems.

Requirements

Only students currently enrolled at Virginia Tech are eligible to apply. Applicants must have a working knowledge of IoT, data processing, and electronic communication methods, and demonstrate exceptional writing skills. The applicant must be able to work independently.

Majors

Computer science, electrical engineering, or related fields of study.

Timeframe:

The position is available immediately and will be available throughout the Fall 2018 / Spring 2019 semesters.

Contact Us

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